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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/635,363	08/06/2003	Alin Theodor Iacob	100-22100	8885
33402	7590	05/02/2005	EXAMINER	
LAW OFFICES OF MARK C. PICKERING P.O. BOX 300 PETALUMA, CA 94953			TRAN, TAN N	
			ART UNIT	PAPER NUMBER
			2826	

DATE MAILED: 05/02/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

# Office Action Summary

Application No.

10/635,363

Applicant(s)

IACOB, ALIN THEODOR

Examiner

TAN N. TRAN

Art Unit

2826

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

## Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

## Status

- 1) ☒ Responsive to communication(s) filed on amendment filed on 02/28/05.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

## Disposition of Claims

- 4) ☒ Claim(s) 1-22 and 31 is/are pending in the application.
- 4a) Of the above claim(s) 3-22 and 31 is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1 and 2 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.



**Minhloan Tran**  
**Primary Examiner**  
**Art Unit 2826**

## Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_\_ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

## Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some \* c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

## Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)  
Paper No(s)/Mail Date \_\_\_\_\_.
- 4) ☐ Interview Summary (PTO-413)  
Paper No(s)/Mail Date. \_\_\_\_\_.
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: \_\_\_\_\_.

## **DETAILED ACTION**

### ***Drawings***

1. The formal drawings filed on 08/25/2003 are acceptable.

### **Information Disclosure Statement**

2. If applicant is aware of any relevant prior art, he/she requested to cite it on form PTO-1449 in accordance with the guidelines set forth in M.P.E.P. 609.

### **Drawings**

3. The drawings are objected to under 37 CFR 1.83(a). The drawings must show every feature of the invention specified in the claims. Therefore, each first saw street extending in a straight line from a point on an edge of the wafer to another point on an edge of the wafer and each second saw street extending in a straight line from a point on an edge of the wafer to another point on an edge of the wafer as recited in claim 1 must be shown or the feature(s) canceled from the claim(s). No new matter should be entered.

A proposed drawing correction or corrected drawings are required in reply to the Office action to avoid abandonment of the application. The objection to the drawings will not be held in abeyance.

***Claim Rejections - 35 USC § 103***

4. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

Claims 1,2 are rejected under 35 U.S.C. 103(a) as being unpatentable over Mok et al. (6,799,976) in view of Byrd et al. (6,809,378).

With regard to claims 1,2, Mok et al. discloses a semiconductor wafer 92 comprising: a plurality of semiconductor circuit 44 formed on the semiconductor wafer 92, each semiconductor circuit 44 having a pair of first sides (FS) that are parallel to each other, and a pair of second sides (SS) that are parallel to each other and perpendicular to the pair of first sides (FS); a plurality of first saw streets 94A that run parallel to the first sides (FS) between a number of the semiconductor circuit 44, each first saw street extending in a straight line from a point on an edge of the wafer 92 to another point on an edge of the wafer 92; a plurality of second saw streets 94B that run parallel to the second sides (SS) between a number of the semiconductor circuit 44, each second saw street 94B extending in a straight line from a point on an edge of the wafer 92 to another point on an edge of the wafer 92. (Note attachment #1, fig. 13 of Mok et al.).

Mok et al. does not disclose a first metal trace formed in a first saw street, the first metal trace not crossing the first saw street; and a second metal trace formed in the first saw street, and the second metal trace not crossing the first saw street, wherein the first saw street formed

between the first semiconductor circuit and second semiconductor circuit; and wherein the first and second metal traces extend across the wafer.

However, Byrd et al. discloses a first conductor trace 1 formed in a first saw street (AA), the first conductor trace 1 not crossing the first saw street (AA); and a second conductor trace 3 formed in the first saw street (AA), and the second conductor trace 3 not crossing the first saw street (AA), wherein the first saw street (AA) formed between the first and second semiconductor circuits 5; and wherein the first and second conductor traces (1,3) extend across the wafer. (Note attachment #2, lines 27-32, column 3, fig. 1(b) of Byrd et al.).

Therefore, it would have been obvious to one of ordinary skill in the art to form the Mok et al.'s device having a first conductor trace formed in a first saw street, the first conductor trace not crossing the first saw street; and a second conductor trace formed in the first saw street, and the second conductor trace not crossing the first saw street, wherein the first saw street formed between the first and second semiconductor circuits; and wherein the first and second conductor traces extend across the wafer such as taught by Byrd et al. in order to provide the power supply to integrated circuit device.

It is inherent that the material of the first and second conductor traces (1,3) to be metallic material in order to provide the power supply to integrated circuit device. Note lines 21-23 in column 7, fig. 5, of Lunde (6,630,685) are cited to support for the inherent position.

***Election/Restrictions***

5. This application contains claims 3-22,31 drawn to an invention nonelected without traverse. A complete reply to the Office Action must include cancelation of nonelected claims for condition to allow the case.

**Response to Amendment**

6. Applicant's arguments with respect to claims 1,2 have been considered but are moot in view of the new ground(s) of rejection.

**Conclusion**

7. Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a). A shortened statutory period for reply to this final action is set to expire **THREE MONTHS** from the mailing date of this action. In the event a first reply is filed within **TWO MONTHS** of the mailing date of this final action and the advisory action is not mailed until after the end of the **THREE-MONTH** shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than **SIX MONTHS** from the date of this final action.

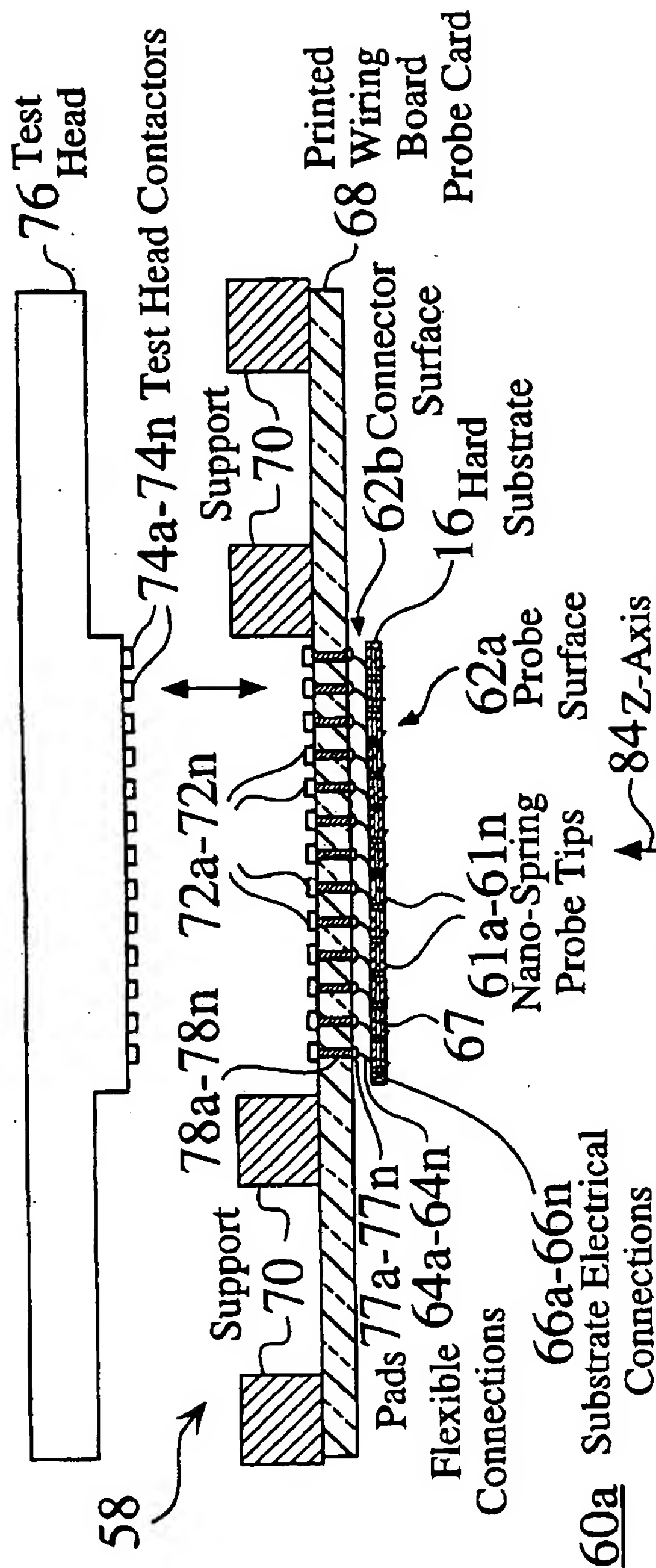
8. Any inquiry concerning this communication or earlier communication from the examiner should be directed to Tan Tran whose telephone number is (571) 272-1923. The examiner can normally be reached on M-F 8:30AM-5PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Nathan Flynn can be reached on (571) 272-1915. The fax phone numbers for the organization where this application or proceeding is assigned are (703) 872-9306 for regular communications and (703) 872-9306 for after final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 305-3900.

TT

April 2005



Attachment # 1



FIG. 1(b)

